

Stefan Donath

Schleifmuehlstr. 6a • 91054 Erlangen • Germany
Tel: +49 9131 976710 • E-mail: Stefan.Donath@donath-productions.de
Nationality: German
www.stefan-donath.de

Job Objective

To contribute and develop my skills and knowledge in research and development of microelectronics and integrated circuits in a high-profile international institute

Key skills and experience

- Experience in analyzing and optimizing power consumption of microelectronic devices at Fraunhofer Institute for Integrated Circuits IIS, Erlangen
- Eight years experience in project-oriented independent working as self-employed person in own business establishment
- Expert knowledge in programming and working with computers
- Strong communication and interpersonal skills

Education

- 02/2005 – present **Elite-Study of Bavarian Graduate School Computational Engineering**
In addition to regular Masters program
Deepening knowledge related to simulation
Experiencing presentation, leading and moderating skills
- 10/2004 – present **Master of Science in Computational Engineering**
Technical Application Field: Design of Microelectronics
University of Erlangen-Nuremberg, Germany
Relevant courses: Design of Mixed-Signal Circuits
Average grade: 1.0 (approximately equivalent to an 'A' average)
Master-Thesis: "Power Optimization Techniques for Integrated Circuits in Deep Submicron and Nanometer ASIC Technologies", Fraunhofer IIS
- 10/2001 – 08/2004 **Bachelor of Science in Computational Engineering**
Technical Application Field: Design of Microelectronics
University of Erlangen-Nuremberg, Germany
Relevant courses: Design of Integrated Digital High Performance ICs
Average grade: 1.9 (approximately equivalent to an 'A' average)
Bachelor-Thesis: "On Optimized Implementations of the Lattice Boltzmann Method on Contemporary High Performance Architectures", RRZE
- 07/2000 **Abitur** (approximate equivalent to High School Diploma)
Gymnasium Burgkunstadt, Germany
Average grade: 2.2 (approximately equivalent to a 'B' average)

Work experience

- 1998 – present **Self-employed person in own business establishment**
- Programming databases for companies
 - Managing and directing meetings with clients
 - Creating customer specifications, problem solving and working project-oriented
- 09/2006 – 01/2007 **Research Assistant at Computer Science Chair for System Simulation (Prof. Rüde), Erlangen, with aim of PhD**
- Research in the field of fluid simulation based on lattice Boltzmann method
 - Co-developed LBM project “WaLBerla”
- 11/2004 – 12/2005 **Assistant at Fraunhofer Institute for Integrated Circuits, Germany**
- Analyzed power consumption of integrated circuits
 - Developed design strategies for power optimization
 - Obtained experience in using Synopsys’ Power Tools
- 10/2003 – 08/2004 **Assistant at Regional Computing Center of Erlangen (RRZE), Germany**
- Researched in techniques for optimizing single processor codes on high performance architectures (especially Intel Xeon, Nocona, Itanium 2)
 - Assisted in developing of a paper for the Super Computing Conference 2004 in Pittsburgh
 - Composed Bachelor Thesis
- 07/2000 – 09/2000 **Internship at BOSCH GmbH, Bamberg, Germany**
- Developed a database for project management
- 10/2000 – 07/2001 **Military replacement service, Red Cross, Lichtenfels, Germany**
- Driver, transported disabled
 - Meal delivery service, delivered meals to disabled at home

Additional skills and experience

Presentations Feb 05: presented results of Bachelor Thesis on SIAM Conference on Computational & Scientific Engineering in Orlando, Florida (USA)
 Aug 05: developed paper for ASIM 2005 Conference Proceedings
 Sep 05: presentation on ASIM 2005 Conference in Erlangen, Germany

Language skills German (native), English (good command)

Computer skills MS-Office (Access, Outlook, Excel, Word, Powerpoint), Databases (Oracle, MSAccess, Firebird), Programming (Pascal/Delphi, Visual Basic, Java, C, C++), Administration (Windows 2000, Windows XP, Windows 2003 Server), Hardware (computer assembly)

Microelectronics Hardware Description Languages (VHDL, SystemC)
 Developing own circuits based on microcontrollers and CMOS-ICs

Memberships and affiliations

Active member of FS_CE, a student organization for Computational Engineering

References available upon request.